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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/587,052	06/02/2000	Timothy John Lindquist	169.17 6630		
5514	7590 02/20/2004	EXAMINER			
FITZPATRICK CELLA HARPER & SCINTO			PAN, DANIEL H		
30 ROCKEFF NEW YORK,	ELLER PLAZA NY 10112		ART UNIT	PAPER NUMBER	
,			2183	a	
			DATE MAILED: 02/20/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

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		Application No.		Applicant(s)			
Office Action Summary		09/587,052	,	LINDQUIST, TIMOTHY JOH	-IN		
		Examiner	120	Art Unit			
			lan	2183			
Period fo	The MAILING DATE of this communication apport	pears on the cover	sheet with the	correspondence address			
A SH THE - Exte after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. It period for reply specified above is less than thirty (30) days, a reply opened for reply is specified above, the maximum statutory period or to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, howev ly within the statutory minin will apply and will expire SI e, cause the application to I	er, may a reply be t num of thirty (30) da X (6) MONTHS fro Decome ABANDON	imely filed bys will be considered timely. In the mailing date of this communication ED (35 U.S.C. § 133).	1.		
Status	•						
1) 又	Responsive to communication(s) filed on 28 N	lovember 2003.					
•	•	s action is non-final	•				
3)[Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
5)⊠ 6)⊠ 7)□	Claim(s) 1-18 is/are pending in the application 4a) Of the above claim(s) is/are withdra Claim(s) 17 and 18 is/are allowed. Claim(s) 1-8,11,12,15,16 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/o	wn from considera					
Applicat	ion Papers						
9)[The specification is objected to by the Examine	er.					
10)	The drawing(s) filed on is/are: a) acc	cepted or b) obje	cted to by the	Examiner.			
	Applicant may not request that any objection to the		-				
11)	Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Extended to be the Extended to	•	=	-	d).		
Priority (under 35 U.S.C. § 119						
a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Burea See the attached detailed Office action for a list	ts have been receives have been receiverity documents have u (PCT Rule 17.2(a	ved. ved in Applica ve been receiv a)).	tion No ved in this National Stage			
Attachmer	nt(s)						
	ce of References Cited (PTO-892)		nterview Summar				
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	۱ 🔲 (5	aper No(s)/Mail I lotice of Informal other:	Patent Application (PTO-152)			

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- 1. Claims 1-16 remain for examination. Claims 17,18 are newly added.
- 2. Claims 1-8, 11,12,15,16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Jennings, III et al. (5,357,152) in view of Chmdani et al. (6,112,019).
- 3. Jennings, III et al. (5,357,152) in view of Chmdani et al. (6,112,019) have been cited in the previous Office action, therefore, copies of these patents are not included.
- 5. As to the newly amended features of "selectively arrange" the processing units in claims 1, 11, Jennings also included selectively arranging the units (e.g. see the programmable selection of the logic networks in fig.4, col.5, lines 63-68, see also the programmable providing enabling signals to the ALUs for multiplication and the enabling signals to the ALUs implementing the add functionality in col.6, lines 29-35).
- 7. The rejections are maintained and incorporated by reference the last Office action on 05/22/03.
- 8. Applicant's amendment filed on 11/28/03 has been fully considered but is not persuasive.
- 9. In the remarks, applicant argued in substance that:
- a) Jennings did not teach dynamically configuration, and the logic network configuration of Jennings is fixed.

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b) Jennings did not have different number of processing units arranged in pipelined layers.

10. As to a) above, Jennings was able to use a programmable circuit to enable the clusters for performing distinct functions (e.g. see one cluster comprising 2 logic networks while the other cluster comprising 3 logic networks in col.4, lines 50-61, see also figs.3,4, see also the selection of desired logic function in col.2, lines 65-67, col.3, lines 1-3). It is not sure how "dynamic" the arrangement the applicant want it be in the claim? Is applicant trying to say that "programmable" is not dynamic? As far as "fixed" limitation argued by applicant, applicant is reminded that unclaimed features cannot be used to overcome prior art CCPA In re Lunderber & Zuschlag, 113 USPQ 530, 534 (1957)). For example, nowhere does applicant's claim recites not fixed, or the like. Nevertheless, Jennings disclosed that his invention contemplates the use of one or more logic networks that can perform a variety of logic functions either by configuration of a multi-function network or by sub-networks each perform one or more dedicated functions (col.1, lines 41-47), and this can permit smaller size programmable logic gate or memory array to be used to control a logic operation of a given complexity, or a given size of array to control more complex operations (col.1, lines 65-68). Therefore, Jennings is very flexible (see also selected desired logic function performed by the logic network in col.2, lines 66-68, see the corresponding enabling signals for ALUS in different clusters in col.6, lines 29-35). Moreover, Jennings logic network can be

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11.

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Office action.

integrated with groups of memory cells which can be selectively activated (e.g. see col.2, lines 7-18), therefore, the configuration of Jennings network is flexible.

- 12. As to b) above, Jennings included one group of clusters (a cluster is a plural logic networks) performing multiplication while the other clusters performing add operation in a pipelined fashion (e.g. see col.5, lines 23-30).
- 13. Claims 9,10,13,14 were objectable in the last Office action (reasons see page 5, lines 1-5 of the last Office action). The limitations of claims 9,10,13,14 have been already added into the newly proposed claims 17,18. Therefore, applicant is kindly reminded not to duplicate the claims in any case.

15. Claims 17,18 are allowable for combining the objectable limitations with the limitations of the parent claims (see the reasons set forth in Page 5, lines 1-5 of the last

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dan Pan whose telephone number is 703 305 9696. The examiner can normally be reached on M-F from 8:30 AM to 4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chan, can be reached on 703 305 9712. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PRIMITY EXAMINER
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